

```

Loop
MRD [addr],[reg]
DBcc Loop

```

[illegible]

```
MRD [addr],[reg]
MRD [addr],[reg]
MRD [addr],[reg]
```

Fig. 1B

PRIOR ART

```
Load Count
REPEAT
MRD [addr],[reg]
```

Fig. 2

100

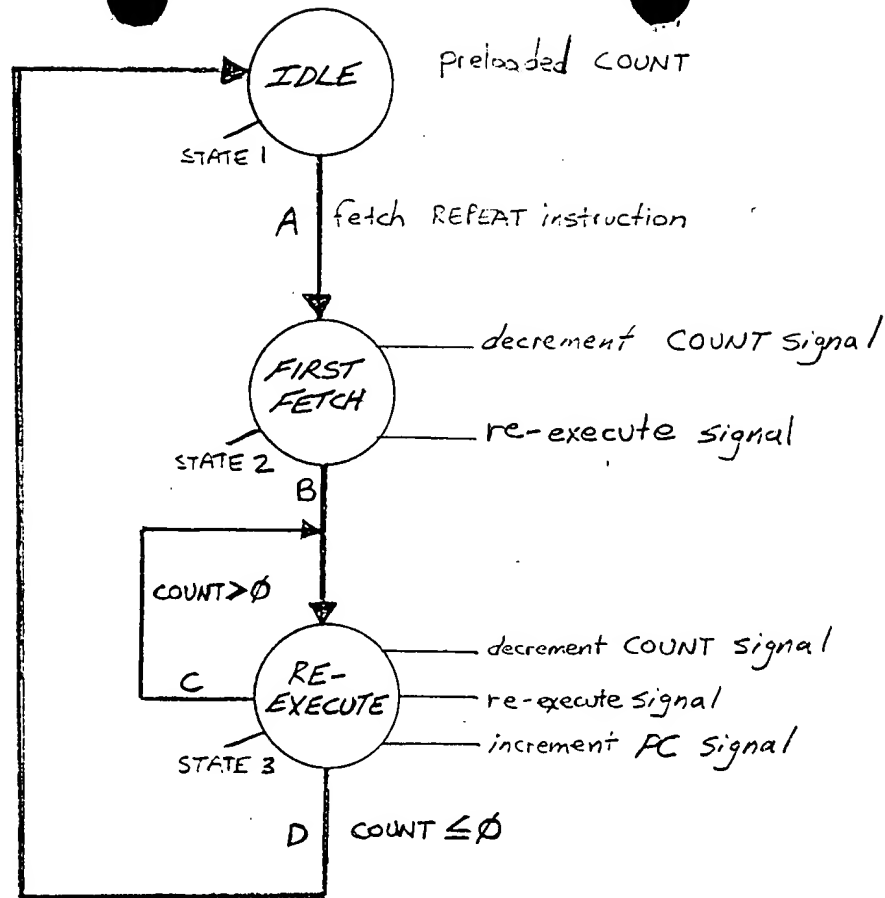


Fig. 4

<u>PROGRAM COUNTER(PC)</u>	<u>TRANSITION</u>	<u>OPERATION</u>
x 10	A	fetch REPEAT instruction
x 11	B	fetch instruction to be repeated (INST _R)
x 11	C	execute INST _R
x 11	C	execute INST _R
x 11	C	execute INST _R
x 11	C	execute INST _R
x 11	C	execute INST _R
x 12	D	fetch next instruction

